

FREQUENCY MULTIPLYING CIRCUIT AND SCREEN DISPLAY DEVICE

Patent Number: JP5299984
Publication date: 1993-11-12
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Requested Patent: ☐ JP5299984
Application Number: JP19920351737 19921208
Priority Number(s):
IPC Classification: H03K5/00
EC Classification:
Equivalents:

Abstract

PURPOSE:To provide a frequency multiplying circuit which can be completely made to one chip without necessitating an outside capacitor, and a screen display device having the circuit.

CONSTITUTION:A multiplication clock generating circuit 210 receives a reference clock A, and rises a multiplication signal B in the timing of the rising and falling of the reference clock A. A coincidence detecting circuit 230 detects that a flip flop 222 operates after receiving the multiplication clock B, and outputs a coincidence detection signal F. Then, a flip flop 211 and the flip flop 222 are initialized, and the multiplication signal B is fallen. Thus, the multiplication clock B having a frequency twice as large as the reference clock A, and a pulse width capable of allowing the flip flop 222 to exactly operate, can be generated without using the capacitor.

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APX 312993